

Docket No.: GR 98 P 1716

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By: h p r

Date: October 4, 2002

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Patrick Heyne et al.
Applic. No. : 09/311,118
Filed : May 13, 1999
Title : Optimized-Delay Multiplexer
Art Unit : 2731

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INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Japanese Utility Model Application JP 61-195126 (inventor not listed), dated December 5, 1986, and English translation of relevant parts;

Japanese Patent Application JP 8-76976 (Igura), dated March 22, 1996, and English Abstract of corresponding U.S. Patent No. 5,687,107;

Japanese Office Action dated June 11, 2002.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,



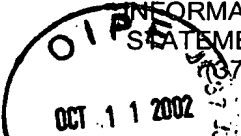
For Applicants

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Date: October 4, 2002

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FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE 	Attorney Docket No.: GR 98 P 1716 Applic. No. 09/311,118 Applicant Patrick Heyne et al. Filing Date May 13, 1999 Group Art Unit 2731
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U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
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	I						

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FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J	61-195126	12/05/86	Japan			X
	K	8-76976	3/22/96	Japan			X
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

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	P	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.